

ABOUT THE INSTITUTE

Delhi Technological University (formerly known as Delhi College of Engineering) located in Delhi is a premier institution of Engineering and Technology in India. The university plays a key role in the national and global knowledge network, empowering India with the wings of knowledge and innovation. The university aims to imbibe a culture of methodical research and to develop a scientific temper for the integration of science, engineering, and management. DTU has been ranked amongst the top institutions in multiple reputed national and international surveys.

ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering has experienced considerable growth since its inception in 1976. The department's vision is to focus on the incubation of innovations in electronic design/ fabrication, and communication technologies, which are needed to address the growing challenges of tomorrow. The overall aim is to harbor a sustainable, and continuously evolving scientific, technological, and educational environment which is both internationally-adapted and industry relevant.

OBJECTIVE OF THE FDP

This program aims to enhance understanding of emerging semiconductor technologies and recent advancements in the field. It focuses on advanced materials, next-generation devices, IC chip design, circuit innovations, reliability, and security aspects. Participants will gain insights into applications such as 5G/6G communication, neuromorphic computing, quantum sensing, in-memory computing, and advanced memory technologies. The program also promotes translational research, strengthens academia-industry collaboration, and equips faculty members, researchers, and students with updated knowledge and practical skills required for effective teaching, quality research, innovation, and future technological development in semiconductor engineering.

The FDP will be conducted in hybrid mode. The expert talks on the first and last day will be organized in physical (offline) mode. The sessions from the second to the fourth day will be conducted in online mode, ensuring flexibility and wider participation.

ONE WEEK FACULTY DEVELOPMENT PROGRAM

(Hybrid Mode)

on

“Recent Developments in Semiconductor Materials, Devices, and Circuits”

May 25-29, 2026



Organized by

Department of Electronics and Communication Engineering

in collaboration with

Vinod Dham Centre of Excellence for Semiconductor and Microelectronics (VDSemiX)

Delhi Technological University, Delhi, 110042

RESOURCE PERSONS

- ❖ Prof. Shaibal Mukhrjee, IIT Indore
- ❖ Prof. Avinash Lahgere, IIT Kanpur
- ❖ Prof. Manoj Kumar, GGSIP University
- ❖ Prof. S Intekhab Amin, JMI Delhi
- ❖ Prof. Navjeet Bagga, IIT Bhubaneshwar
- ❖ Prof. Arjun Singh Yadav, NIT Rourkela
- ❖ Prof. Arti Noor, IIIT Noida
- ❖ Prof. Chandramauleshwar Roy, VIT Chennai
- ❖ Mr. Manoj Tiwari, Principal Engineer Synopsys ind , Noida
- ❖ Dr. Susha Lekshmi, Senior Program Manager, IHFC (TIH of IIT Delhi)

TOPICS TO BE COVERED

- ✓ Analog/RF device and circuit design for 5G/6G applications
- ✓ Recent development in IC chip and security
- ✓ Emerging Semiconductor Device for Neuromorphic computing
- ✓ Quantum Sensor for Sustainable Healthcare, Environment and Agriculture
- ✓ In memory computation architecture for GPU core
- ✓ Emerging Semiconductor Memory and Technology
- ✓ The Reliability-aware semiconductor devices
- ✓ MOS to data transfer building block
- ✓ Translational Research for Sustainable Technologies and Innovation

ORGANIZING COMMITTEE

Chief Patron

Prof. Prateek Sharma
Vice Chancellor, DTU

Convenors

Prof. Neeta Pandey
HOD (ECE), DTU

Prof. Rishu Chaujar
Director, VDSemiX, DTU

Coordinator

Dr. Sumit Kale
Asst. Prof., ECE Dept., DTU

Organizing Member

Dr. Sonam Rewari, Asst. Prof., ECE Dept.
Mr. Sachin Dhariwal, Asst. Prof., ECE Dept.
Dr. Sonal Singh, Asst. Prof., ECE Dept.
Dr. M Ganesh, Asst. Prof., ECE Dept.

IMPORTANT DATES

Last Date of Registration: May 21, 2026
Confirmation to Participants: May 24, 2026
Date of FDP: May 25-29, 2026

For any query please contact:

Dr. Sumit Kale

Email: sumit.kale@dtu.ac.in

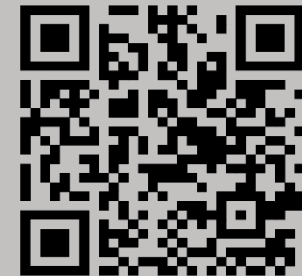
Contact no. 09926337025

PARTICIPANTS

The program is open to the faculty members of AICTE/ UGC approved Engineering Institutes/Universities, and PhD scholars.

REGISTRATION DETAILS

- ✓ The number of participants is limited to 40 only. (first come first serve basis).
- ✓ No registration fees required.
- ✓ **Registration Link/QR Code:**
<https://forms.gle/NBD7CPj6JSffkXX9A>



Note: Certificates will be issued to the participants on successful participation in the FDP.