

Section: 4A	Department of Electronics and Communication Engineering Delhi Technological University Time Table (EVEN Sem 2024-25) w.e.f 1st January 2025									
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Day	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-01:00	01:00-02:00	02:00-03:00	03:00-04:00	04:00-05:00	05:00-06:00
Monday			EC210_LAB_G3_RP_EDC-II_LAB EC204_LAB_G2_AKG_DE_LAB EC206_LAB_G1_RB_TELECOM_LAB			EC208_L_JP_R2	EC202_L_MSC_R4	EC202_TU_G2_MSC_R4		
Tuesday			EC210_LAB_G1_RP_EDC-II_LAB EC206_LAB_G3_RB_TELECOM_LAB EC208_LAB_G2_JP_LIC_LAB		EC208_L_JP_R4	EC210_L_RP_R4		EC206_L_RB_R4		
Wednesday			EC204_LAB_G1_AKG_DE_LAB EC206_LAB_G2_RB_TELECOM_LAB EC208_LAB_G3_JP_LIC_LAB		EC210_L_RP_R4		EC206_L_RB_R4	EC208_L_JP_R4	EC204_L_AKG_R4	
Thursday			EC204_L_AKG_R4	EC204_L_AKG_R4		EC210_L_RP_R4	EC202_L_MSC_R4	EC202_TU_G3_MSC_R4		
Friday				EC202_TU_G1_MSC_R4	EC202_L_MSC_R4		EC210_LAB_G2_RP_EDC-II_LAB EC204_LAB_G3_AKG_DE_LAB EC208_LAB_G1_JP_LIC_LAB		EC206_L_RB_R4	

L-Lecture, TU-Tutorial, Lab- Laboratory, G1- Group 1, G2- Group2. G3- Group 3, RX:- Room No. 'X'

Faculty Name Abbreviation	Faculty Name	Course Abbreviation	Course
MSC	Prof. M. S. Chaudhary	EC202	Electromagnetics
AKG	Dr. Ajai Kumar Gautam	EC204	Digital Design – II
RB	Dr. Rajesh Birok	EC206	Digital Communication
JP	Prof. J. Panda	EC208	Computer Architecture
RP	Prof. Rajeshwari Pandey	EC210	Analog Electronics – II

Mr. Akshay Mann AM

(Incharge, Time-Table Committee, ECE Dept.)

Section: 4B	Department of Electronics and Communication Engineering Delhi Technological University Time Table (EVEN Sem 2024-25) w.e.f 1st January 2025									
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Day	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-01:00	01:00-02:00	02:00-03:00	03:00-04:00	04:00-05:00	05:00-06:00
Monday			EC208_L_DK_R5	EC208_L_DK_R5	EC202_L_AC_R5	EC202_TU_G3_A C_R5		EC206_L_SM_R5		
Tuesday			EC210_L_SD_R5	EC208_L_DK_R5	EC210_LAB_G1_AKS_EDC-II_LAB EC206_LAB_G3_SM_TELECOM_LAB EC208_LAB_G2_DK_LIC_LAB			EC204_L_KS_R5		
Wednesday			EC202_L_AC_R5	EC202_TU_G1_AC_R5	EC204_LAB_G1_KS_DE_LAB EC206_LAB_G2_SM_TELECOM_LAB EC208_LAB_G3_DK_LIC_LAB			EC204_L_KS_R5		
Thursday			EC210_LAB_G3_RP_EDC-II_LAB EC204_LAB_G2_KS_DE_LAB EC206_LAB_G1_PT_TELECOM_LAB		EC206_L_SM_R5		EC210_L_SD_R5	EC210_L_SD_R5		
Friday			EC202_L_AC_R5	EC202_TU_G2_AC_R5	EC206_L_SM_R5		EC204_L_KS_R5		EC210_LAB_G2_PD_EDC-II_LAB EC204_LAB_G3_KS_DE_LAB EC208_LAB_G1_RR_LIC_LAB	

L-Lecture, TU-Tutorial, Lab- Laboratory, G1- Group 1, G2- Group2. G3- Group 3, RX:- Room No. 'X'

Faculty Name Abbreviation	Faculty Name	Course Abbreviation	Course	Faculty Name Abbreviation	Faculty Name
AC	Dr. Anurag Chauhan	EC202	Electromagnetics		
KS	Dr. Kriti Suneja	EC204	Digital Design – II		
SM	Dr. Sudipta Majumdar	EC206	Digital Communication	PT	Mr. Piyush Tewari
DK	Prof. Dinesh Chutani	EC208	Computer Architecture		
SD	Mr. Sachin Dhariwal	EC210	Analog Electronics – II		

(Incharge, Time-Table Committee, ECE Dept.)

Section 4C	Department of Electronics and Communication Engineering Delhi Technological University Time Table (EVEN Sem 2024-25) w.e.f 1st January 2025									
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Day	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-01:00	01:00-02:00	02:00-03:00	03:00-04:00	04:00-05:00	05:00-06:00
Monday			EC204_L_AM_R3	EC208_L_RR_R3	EC202_L_SKH_R7		EC210_LAB_G3_SK_EDC-II_LAB EC204_LAB_G2_AM_DE_LAB EC206_LAB_G1_AR_TELECOM_LAB			
Tuesday			EC210_L_SK_R3	EC210_L_SK_R3	EC208_L_RR_R5		EC206_L_AR_R3	EC202_TU_G3_SKH_R6		
Wednesday			EC202_TU_G2_SKH_R3	EC202_L_SKH_R3	EC208_L_RR_R5		EC204_LAB_G1_AM_DE_LAB EC206_LAB_G2_AR_TELECOM_LAB EC208_LAB_G3_RR_LIC_LAB			
Thursday			EC202_L_SKH_R3	EC202_TU_G1_SKH_R6	EC210_LAB_G2_SD_EDC-II_LAB EC204_LAB_G3_AM_DE_LAB EC208_LAB_G1_RR_LIC_LAB		EC206_L_AR_R3			
Friday			EC204_L_AM_R3	EC204_L_AM_R3	EC210_LAB_G1_SK_EDC-II_LAB EC206_LAB_G3_AR_TELECOM_LAB EC208_LAB_G2_RR_LIC_LAB		EC210_L_SK_R3		EC206_L_AR_R3	

L-Lecture, TU-Tutorial, Lab- Laboratory, G1- Group 1, G2- Group2, G3- Group 3, RX:- Room No. 'X'

Faculty Name Abbreviation	Faculty Name	Course Abbreviation	Course	Faculty Name Abbreviation	Faculty Name
SKH	Mr. Sumit Kumar Khandelwal	EC202	Electromagnetics		
AM	Mr. Akshay Maan	EC204	Digital Design – II	AKG	AJAI KUMAR GAUTAM
AR	Dr. Avinash Ratre	EC206	Digital Communication		
RR	Prof. Rajesh Rohilla	EC208	Computer Architecture		
SK	Dr. Sumit kale	EC210	Analog Electronics – II		

(Incharge, Time-Table Committee, ECE Dept.)

Section 4D	Department of Electronics and Communication Engineering Delhi Technological University Time Table (EVEN Sem 2024-25) w.e.f 1st January 2025									
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Day	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-01:00	01:00-02:00	02:00-03:00	03:00-04:00	04:00-05:00	05:00-06:00
Monday			EC210_L_SKH_R7	EC202_TU_G3_DS_R7	EC204_LAB_G1_KS_DE_LAB EC206_LAB_G2_SM_TELECOM_LAB EC208_LAB_G3_DK_LIC_LAB		EC204_L_AKG_R7	EC202_TU_G1_DS_R7	EC210_LAB_G2_SD_EDC-II_LAB EC204_LAB_G3_AM_DE_LAB EC208_LAB_G1_MB_LIC_LAB	
Tuesday			EC206_L_PT_R7	EC206_L_PT_R7	EC208_L_MB_R7		EC204_L_AKG_R7	EC202_TU_G1_DS_R7		
Wednesday				EC208_L_MB_R7	EC202_L_DS_R7		EC210_L_SKH_R7	EC204_L_AKG_R7	EC210_LAB_G1_AKS_EDC-II_LAB EC206_LAB_G3_PT_TELECOM_LAB EC208_LAB_G2_MB_LIC_LAB	
Thursday			EC202_L_DS_R7	EC202_TU_G2_DS_R7	EC206_L_PT_R7					
Friday			EC210_LAB_G3_SD_EDC-II_LAB EC204_LAB_G2_AKG_DE_LAB EC206_LAB_G1_AR_TELECOM_LAB		EC210_L_SKH_R7		EC208_L_MB_R7	EC202_L_DS_R7		

L-Lecture, TU-Tutorial, Lab- Laboratory, G1- Group 1, G2- Group2, G3- Group 3, RX:- Room No. 'X'

Faculty Name Abbreviation	Faculty Name	Course Abbreviation	Course	Faculty Name Abbreviation	Faculty Name
DS	Dr. Deepika Sipal	EC202	Electromagnetics		
AKG	Dr. Ajai Kumar Gautam	EC204	Digital Design – II		
PT	Mr. Piyush Tiwari	EC206	Digital Communication	SM	Dr. Sudipta Majumdar

MB	Dr. Malti Bansal	EC208	Computer Architecture		
SKH	Mr. Sumit Kumar Khandelwal	EC210	Analog Electronics – II		

(Incharge, Time-Table Committee, ECE Dept.)

Section: 6A	Department of Electronics and Communication Engineering Delhi Technological University Time Table (EVEN Sem 2024-25) w.e.f 1st January 2025									
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Day	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-01:00	01:00-02:00	02:00-03:00	03:00-04:00	04:00-05:00	05:00-06:00
Monday	EC310_L_KS_R2	EC310_L_KS_R2			EC304_LAB_G3_AP_ADSPLAB EC302_LAB_G1_SK_VLSI_LAB EC306_LAB_G2_LG_MP-II_LAB		EC306_L_LG_R2	EC320_L_S1_MG_R1 EC320_L_S2_RT_R2	EC320_L_S1_MG_R1 EC320_L_S2_RT_R2	
Tuesday	EC342_L_NJ_R2	EC342_L_NJ_R2	EC304_L_AP_R8	EC304_L_AP_R8			EC306_L_LG_R1	EC302_L_SK_R1	EC302_L_SK_R1	
Wednesday	EC310_L_KS_R2	EC342_L_NJ_R2	EC332_L_S1_LT_R1 EC332_L_S2_SM_R2	EC332_L_S1_LT_R1 EC332_L_S2_SM_R2			EC328_L_ST_R1	EC328_L_ST_R1		
Thursday	EC342_TU_G1_NJ_R2	EC342_TU_G2_NJ_R2	EC332_L_S1_LT_R1 EC332_L_S2_SM_R2	EC306_L_LG_R1	EC304_LAB_G2_RT_ADSPLAB EC302_LAB_G3_SK_VLSI_LAB EC306_LAB_G1_LG_MP-II_LAB		EC304_L_AP_R7	EC302_L_SK_R1		
Friday	EC310_TU_G1_KS_R2	EC310_TU_G2_KS_R2	EC332_TU_S1_G1_LT_R1 EC332_TU_S2_G1_SM_R2	EC332_TU_S1_G2_LT_R1 EC332_TU_S2_G2_SM_R2	EC304_LAB_G1_RT_ADSPLAB EC302_LAB_G2_NP_VLSI_LAB EC306_LAB_G3_LG_MP-II_LAB		EC328_L_ST_R1	EC328_TU_ST_R1	EC320_L_S1_MG_R1 EC320_L_S2_RT_R2	EC320_L_S1_MG_R1 EC320_L_S2_RT_R2

L-Lecture, TU-Tutorial, Lab- Laboratory, G1- Group 1, G2- Group2, G3- Group 3, RX:- Room No. 'X'

Faculty Name Abbreviation	Faculty Name	Course Abbreviation	Course	Faculty Name Abbreviation	Faculty Name
SK	Dr. Sumit Kale	EC 302	VLSI Design		
AP	Dr. Anukul Pandey	EC 304	Digital Signal Processing		
LG	Mr. Lokesh Gautam	EC306	Embedded Systems		
KS	Dr. Kriti Suneja	EC 310 (E1)	Testing & Diagnosis of Digital System Design		
MG	Mr. M. Ganesh	EC 320 (E2)	Soft Computing	RT	Mr. Rahul Thakur
ST	Dr. Sachin Taran	EC328 (E3)	Speech Recognition		
LT	Ms. Lavi Tanwar	EC332(E4)	Information Theory and Coding	SM	Dr. Sudipta Majumdar
NJ	Dr. N. Jayanti	EC342(E5)	Machine Learning		

(Incharge, Time-Table Committee, ECE Dept.)

Section: 6B	Department of Electronics and Communication Engineering Delhi Technological University Time Table (EVEN Sem 2024-25) w.e.f 1st January 2025									
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Day	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-01:00	01:00-02:00	02:00-03:00	03:00-04:00	04:00-05:00	05:00-06:00
Monday	EC310_L_KS_R2	EC310_L_KS_R2	MG302_L_Khusboo_R2	MG302_L_Khusboo_R2	EC306_L_SI_R2		EC304_LAB_G3_CD_ADSPLAB EC302_LAB_G1_DN_VLSI_LAB EC306_LAB_G2_SI_MP-II_LAB	EC320_L_S1_MG_R1 EC320_L_S2_RT_R2	EC320_L_S1_MG_R1 EC320_L_S2_RT_R2	
Tuesday	EC342_L_NJ_R2	EC342_L_NJ_R2	EC304_LAB_G2_CD_ADSPLAB EC302_LAB_G3_DN_VLSI_LAB EC306_LAB_G1_SI_MP-II_LAB		EC306_L_SI_R2	EC304_L_CD_R1	EC304_LAB_G1_RT_ADSPLAB EC302_LAB_G2_DN_VLSI_LAB EC306_LAB_G3_SI_MP-II_LAB	MG302_L_Khusboo_R2		
Wednesday	EC310_L_KS_R2	EC342_L_NJ_R2	EC332_L_S1_LT_R1 EC332_L_S2_SM_R2	EC332_L_S1_LT_R1 EC332_L_S2_SM_R2	EC306_L_SI_R2	EC302_L_DN_R1	EC328_L_ST_R1	EC328_L_ST_R1		
Thursday	EC342_TU_G1_NJ_R2	EC342_TU_G2_NJ_R2	EC332_L_S1_LT_R1 EC332_L_S2_SM_R2	EC304_L_CD_R3	EC302_L_DN_R3				EC304_L_CD_R2	
Friday	EC310_TU_G1_KS_R2	EC310_TU_G2_KS_R2	EC332_TU_S1_G1_LT_R1 EC332_TU_S2_G1_SM_R2	EC332_TU_S1_G2_LT_R1 EC332_TU_S2_G2_SM_R2	EC302_L_DN_R3		EC328_L_ST_R1	EC328_TU_ST_R1	EC320_L_S1_MG_R1 EC320_L_S2_RT_R2	EC320_L_S1_MG_R1 EC320_L_S2_RT_R2

L-Lecture, TU-Tutorial, Lab- Laboratory, G1- Group 1, G2- Group2, G3- Group 3, RX:- Room No. 'X'

Faculty Name Abbreviation	Faculty Name	Course Abbreviation	Course	Faculty Name Abbreviation	Faculty Name
DN	Dr. Devanand	EC 302	VLSI Design		
CD	Dr. Chhavi Dhiman	EC 304	Digital Signal Processing		
SI	Prof. S. Indu	EC306	Embedded Systems		
KS	Dr. Kriti Suneja	EC 310 (E1)	Testing & Diagnosis of Digital System Design		
MG	Mr. M. Ganesh	EC 320 (E2)	Soft Computing	RT	Mr. Rahul Thakur
ST	Dr. Sachin Taran	EC328 (E3)	Speech Recognition		
LT	Ms. Lavi Tanwar	EC332(E4)	Information Theory and Coding	SM	Dr. Sudipta Majumdar
NJ	Dr. N. Jayanti	EC342(E5)	Machine Learning		

(Incharge, Time-Table Committee, ECE Dept.)

Section: 6C	Department of Electronics and Communication Engineering Delhi Technological University Time Table (EVEN Sem 2024-25) w.e.f 1st January 2025
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Day	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-01:00	01:00-02:00	02:00-03:00	03:00-04:00	04:00-05:00	05:00-06:00
Monday	EC310_L_KS_R2	EC310_L_KS_R2	MG302_L_Bhavika_R1		EC302_L_NP_R3			EC304_L_MK_R1	EC320_L_S1_MG_R1 EC320_L_S2_RT_R2	EC320_L_S1_MG_R1 EC320_L_S2_RT_R2
Tuesday	EC342_L_NJ_R2	EC342_L_NJ_R2	EC306_L_VS_R2	EC306_L_VS_R2	EC304_LAB_G1_MK_ADSPLAB EC302_LAB_G2_NP_VLSI_LAB EC306_LAB_G3_VS_MP-II_LAB		MG302_L_Bhavika_R2	MG302_L_Bhavika_R2		
Wednesday	EC310_L_KS_R2	EC342_L_NJ_R2	EC332_L_S1_LT_R1 EC332_L_S2_SM_R2	EC332_L_S1_LT_R1 EC332_L_S2_SM_R2	EC304_LAB_G2_MK_ADSPLAB EC302_LAB_G3_NP_VLSI_LAB EC306_LAB_G1_LG_MP-II_LAB		EC328_L_ST_R1	EC328_L_ST_R1		
Thursday	EC342_TU_G1_NJ_R2	EC342_TU_G2_NJ_R2	EC332_L_S1_LT_R1 EC332_L_S2_SM_R2	EC306_L_VS_R2	EC302_L_NP_R1				EC304_LAB_G3_MK_ADSPLAB EC302_LAB_G1_NP_VLSI_LAB EC306_LAB_G2_LG_VS-II_LAB	
Friday	EC310_TU_G1_KS_R2	EC310_TU_G2_KS_R2	EC332_TU_S1_G1_LT_R1 EC332_TU_S2_G1_SM_R2	EC332_TU_S1_G2_LT_R1 EC332_TU_S2_G2_SM_R2	EC304_L_MK_R1	EC304_L_MK_R1	EC328_L_ST_R1	EC328_TU_ST_R1	EC320_L_S1_MG_R1 EC320_L_S2_RT_R2	EC320_L_S1_MG_R1 EC320_L_S2_RT_R2

L-Lecture, TU-Tutorial, Lab- Laboratory, G1- Group 1, G2- Group2. G3- Group 3, RX:- Room No. 'X'

Faculty Name Abbreviation	Faculty Name	Course Abbreviation	Course	Faculty Name Abbreviation	Faculty Name
NP	Prof. Neeta Pandey	EC 302	VLSI Design		
MK	Dr. Manjeet Kumar	EC 304	Digital Signal Processing		
VS	Mr. Varun Sangwan	EC306	Embedded Systems		
KS	Dr. Kriti Suneja	EC 310 (E1)	Testing & Diagnosis of Digital System Design		
MG	Mr. M. Ganesh	EC 320 (E2)	Soft Computing	RT	Mr. Rahul Thakur
ST	Dr. Sachin Taran	EC328 (E3)	Speech Recognition		
LT	Ms. Lavi Tanwar	EC332(E4)	Information Theory and Coding	SM	Dr. Sudipta Majumdar
NJ	Dr. N. Jayanti	EC342(E5)	Machine Learning		

(Incharge, Time-Table Committee, ECE Dept.)

Section: 6D	Department of Electronics and Communication Engineering Delhi Technological University Time Table (EVEN Sem 2024-25) w.e.f 1st January 2025
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Day	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-01:00	01:00-02:00	02:00-03:00	03:00-04:00	04:00-05:00	05:00-06:00
Monday	EC310_L_KS_R2	EC310_L_KS_R2	EC304_LAB_G3_CD_ADSPLAB EC302_LAB_G1_SK_VLSI_LAB EC306_LAB_G2_LG_MP-II_LAB		EC306_L_MK_R8	EC306_L_MK_R8			EC320_L_S1_MG_R1 EC320_L_S2_RT_R2	EC320_L_S1_MG_R1 EC320_L_S2_RT_R2
Tuesday	EC342_L_NJ_R2	EC342_L_NJ_R2			EC302_L_DN_R3				EC304_LAB_G1_CD_ADSPLAB EC302_LAB_G2_DN_VLSI_LAB EC306_LAB_G3_ST_MP-II_LAB	

Wednesday	EC310_L_KS_R2	EC342_L_NJ_R2	EC332_L_S1_LT_R1 EC332_L_S2_SM_R2	EC332_L_S1_LT_R1 EC332_L_S2_SM_R2	EC302_L_DN_R3	EC304_L_CD_R3	EC328_L_ST_R1	EC328_L_ST_R1		
Thursday	EC342_TU_G1_NJ_R2	EC342_TU_G2_NJ_R2	EC332_L_S1_LT_R1 EC332_L_S2_SM_R2		EC306_L_MK_R2		EC304_LAB_G2_CD_ADSP EC302_LAB_G3_DN_VLSI EC306_LAB_G1_VS_MP-II			
Friday	EC310_TU_G1_KS_R2	EC310_TU_G2_KS_R2	EC332_TU_S1_G1_LT_R1 EC332_TU_S2_G1_SM_R2	EC332_TU_S1_G2_LT_R1 EC332_TU_S2_G2_SM_R2	EC304_L_CD_R2		EC328_L_ST_R1	EC328_TU_ST_R1	EC320_L_S1_MG_R1 EC320_L_S2_RT_R2	EC320_L_S1_MG_R1 EC320_L_S2_RT_R2

L-Lecture, TU-Tutorial, Lab- Laboratory, G1- Group 1, G2- Group2. G3- Group 3, RX:- Room No. 'X'

Faculty Name Abbreviation	Faculty Name	Course Abbreviation	Course	Faculty Name Abbreviation	Faculty Name
DN	Dr. Devanand	EC 302	VLSI Design		
CD	Dr. Chhavi Dhiman	EC 304	Digital Signal Processing		
MK	Dr. Manjeet Kumar	EC306	Embedded Systems		
KS	Dr. Kriti Suneja	EC 310 (E1)	Testing & Diagnosis of Digital System Design		
MG	Mr. M. Ganesh	EC 320 (E2)	Soft Computing	RT	Mr. Rahul Thakur
ST	Dr. Sachin Taran	EC328 (E3)	Speech Recognition		
LT	Ms. Lavi Tanwar	EC332(E4)	Information Theory and Coding	SM	Dr. Sudipta Majumdar
NJ	Dr. N. Jayanti	EC342(E5)	Machine Learning		

(Incharge, Time-Table Committee, ECE Dept.)

Department of Electronics and Communication Engineering Delhi Technological University Time Table (EVEN Sem 2024-25) w.e.f 1st January 2025										
Section 8C, 8D, 8E & 8F	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-01:00	01:00-02:00	02:00-03:00	03:00-04:00	04:00-05:00	05:00-06:00
Monday										
Tuesday										
Wednesday										
Thursday										
Friday										

L-Lecture, TU-Tutorial, Lab- Laboratory, G1- Group 1, G2- Group2. G3- Group 3, RX:- Room No. 'X'

Faculty Name Abbreviation	Faculty Name	Course Abbreviation	Course Code & Name

(Incharge, Time-Table Committee, ECE Dept.)

Department of Electronics and Communication Engineering Delhi Technological University Time Table (EVEN Sem 2024-25) w.e.f 1st January 2025										
Section M, Tech VLSI 2ND SEM	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-01:00	01:00-02:00	02:00-03:00	03:00-04:00	04:00-05:00	05:00-06:00
Monday		VLS504_L_AM_R9	VLS502_L_VK_R9	VLS534_L_RT_R9	VLS532_L_SR_R9		VLS534_L_RT_R9		VLS502_LAB_VK_VLSI_LAB	
Tuesday	VLS504_LAB_AM_VLSI_LAB		VLS504_L_AM_R9	VLS504_L_AM_R9		VLS532_L_SR_R9	VLS532_L_SR_R9			
Wednesday			VLS532_SR_VLSI_LAB		UCC502_L_RKU_R9		VLS534_LAB_RT_VLSI_LAB		VLS546_VK_VLSI_LAB	
Thursday		VLS534_L_RT_R9	VLS546_VK_VLSI_LAB			VLS502_L_VK_R9				
Friday				UCC502_L_RKU_R9				VLS502_L_VK_R9		

L-Lecture, TU-Tutorial, Lab- Laboratory, RX:- Room No. 'X'

Faculty Name Abbreviation	Faculty Name	Course Abbreviation	Course Name
VK	Mr. Vinay Kumar	VLS502	Low Power VLSI Design
AM	Mr. Akshay Mann	VLS504	Mixed Signal Design
SR	Dr. Sonam Rewari	VLS532	Advanced Computer Architecture
RT	Mr. Rahul Thakur	VLS534	Machine Learning in VLSI Design
RKU	Dr. Rohit Kumar	UCC502	Research Methodology and IPR
VK	Mr. Vinay Kumar	VLS546	<i>Skill Enhancement Course</i>

(Incharge, Time-Table Committee, ECE Dept.)

Section M. Tech SPDD 2ND SEM										
Department of Electronics and Communication Engineering Delhi Technological University Time Table (EVEN Sem 2024-25) w.e.f 1st January 2025										
Day	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-01:00	01:00-02:00	02:00-03:00	03:00-04:00	04:00-05:00	05:00-06:00
Monday			SPD504_L_AR_R8		SPD534_L_SR_R9					
Tuesday					SPD532_L_PD_R8	SPD534_L_SR_R9	SPD534_L_SR_R9		SPD546_AP_TELECOM_LAB	
Wednesday					UCC502_L_ST_R8		SPD532_LAB_PD_ADSP_LAB		SPD504_LAB_AR_ADSP_LAB	
Thursday				SPD504_L_AR_R8	SP502_L_AP_R8		SPD532_L_PD_R8	SPD532_L_PD_R8	SPD502_LAB_AP_TELECOM_LAB	
Friday			UCC502_L_ST_R8		SP502_L_AP_R8	SP502_L_AP_R8	SPD534_SR_VLSI_LAB		SPD546_AP_TELECOM_LAB	

L-Lecture, TU-Tutorial, Lab- Laboratory, RX:- Room No. 'X'

Faculty Name Abbreviation	Faculty Name	Course Abbreviation	Course
AP	Dr. Anukul Pandey	SPD502	DSP Algorithms and Architectures
AR	Dr. Avinash Ratre	SPD504	Detection & Estimation Theory
PD	Dr. Pankaj Dahiya	SPD532	Soft Computing
SR	Dr. Sonam Rewari	SPD534	Advance Computer Architecture
ST	Dr. Sachin Taran	UCC502	Research Methodology and IPR
AP	Dr. Anukul Pandey	SPD546	Skill Enhancement Course

(Incharge, Time-Table Committee, ECE Dept.)

Section M. Tech MOC 2ND SEM										
Department of Electronics and Communication Engineering Delhi Technological University Time Table (EVEN Sem 2024-25) w.e.f 1st January 2025										
Day	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-01:00	01:00-02:00	02:00-03:00	03:00-04:00	04:00-05:00	05:00-06:00
Monday		MOC534_L_MG_R8	MOC532_L_YS_R4		MOC504_LAB_DS_MW_LAB		MOC534_LAB_MG_MW_LAB			
Tuesday		MOC504_L_DS_R8			MOC546_YS_MW_LAB			MOC502_L_KRS_R9	MOC502_L_KRS_R9	
Wednesday		MOC534_L_MG_R8	MOC502_L_KRS_R9		UCC502_L_ST_R8		MOC546_YS_MW_LAB			
Thursday			MOC502_LAB_KRS_MW_LAB		MOC534_L_MG_R9		MOC504_L_DS_R9	MOC504_L_DS_R9		
Friday			UCC502_L_ST_R8			MOC532_L_YS_R4				

L-Lecture, TU-Tutorial, Lab- Laboratory, RX:- Room No. 'X'

Faculty Name Abbreviation	Faculty Name	Course Abbreviation	Course
KRS	Mr. Kaustubh Ranjan Singh	MOC502	Optical Electronics
DS	Dr. Deepika Sipal	MOC504	Microwave Active Circuits
YS	Dr. Yashna Sharma	MOC532	Solar Photovoltaics
MG	Mr. M. Ganesh	MOC534	Smart Antenna
ST	Dr. Sachin Taran	UCC502	Research Methodology and IPR
YS	Dr. Yashna Sharma	MOC546	<i>Skill Enhancement Course</i>

(Incharge, Time-Table Committee, ECE Dept.)

Section: Evening 8th Sem	Department of Electronics and Communication Engineering Delhi Technological University Time Table (EVEN Sem 2024-25) w.e.f 1st January 2025
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Day	06:00-07:00	07:00-08:00	08:00-09:00
Monday	CEC 412_LAB_NJ_R3		CEC 412_L_NJ_R3
Tuesday	CEC 410_L_HK_R3	CEC 410_L_HK_R3	CEC 410_L_TU_R3
Wednesday	CEC 410_L_HK_R3	CEC402_L_RB_R3	CEC402_L_RB_R3
Thursday	CEC402_LAB_RB TELECOM_LAB		CEC402_L_RB_R3
Friday	CEC 412_L_NJ_R3	CEC 412_L_NJ_R3	
L-Lecture, TU-Tutorial, Lab- Laboratory, G1- Group 1, G2- Group2			

Faculty Name Abbreviation	Faculty Name	Course Abbreviation	Course
RB	Dr. Rajesh Birok	CEC 402	Radar and Sattelite Communication
HK	Dr. Harikesh	CEC 410	Advance coding theory
NJ	Dr. N. Jayanti	CEC 412	Machine learning

(Incharge, Time-Table Committee, ECE Dept.)