

DEPARTMENT OF INFORMATION TECHNOLOGY DELHI TECHNOLOGICAL UNIVERSITY

(Established by Govt. of Delhi vide Act 6 of 2009) (Formerly Delhi College of Engineering) SHAHBAD DAULATPUR, BAWANA ROAD, DELHI-110042

File No.: DTU/IT/814

Walk-in Interview for Engagement of Guest Faculty Positions

Department of Information Technology, Delhi Technological University invites applications for the engagement of guest faculty on a purely temporary basis for the upcoming even semester session commencing from January'2024 to June'2024 and payment will be made on an hourly basis as per the university norms (Annexure-I). Interested candidate may fill out the online application form https://forms.gle/Qkq4T2oVBVaAB12y5 and attend the Walkin interview on 27.12.2023 at 10:00am onwards in the Committee Room of Information Technology Department (AB3 Building). In case of a large number of applications, the department may adopt shortlisting/screening criteria based on the credentials of the candidates. The required essential qualifications are enclosed as Annexure-II. To have easy access of the online form, please scan the QR code:



Guest faculty is required for the following subjects:

- Programming Fundamentals
- Object Oriented Programming
- Operating Systems
- Algorithm Design Analysis
- Software Engineering
- Cyber Forensics
- High Speed Networks
- Big Data

Date: 20/12/2023

- Web Technology
- High Performance Computing
- Computer Network and Architecture

- Discrete Structures
- Database Management Systems
- Computer Organization Architecture
- Compiler Design
- Artificial Intelligence & Expert System
- Deep Learning
- Big Data
- Computer Vision
- Ethical Hacking
- Mobile and Digital Forensics

Prof. Dinesh K. Vishwakarma Head of Department,

Information Technology, DTU



DELHI TECHNOLOGICAL UNIVERSITY

(Formerly Delhi College of Engineering) SHAHABAD DAULATPUR, BAWANA ROAD, DELHI-110042

F.No.2- 592/2013/Estt/DTU/

Dated 37/09/2022

NOTIFICATION

This is in supersession of earlier notifications/orders regarding honorarium and TA/Conveyance charges of Honorary/Guest/Visiting Faculty. On the recommendation of Finance Committee, the Board of Management in its 48th meeting held on 18.08.2022 vide agenda no. 48.11 approved the enhancement in the honorarium and TA/Conveyance charges being paid to the Honorary/Guest/Visiting Faculty w.e.f. academic year 2022-23, as per the details given below:

S. No.	Designation	Approved Rates/per hr		Max Ceiling per
		Theory	Lab	month in rupees
1.	Professor	Rs. 2750/-	Rs. 1375/-	Rs. 70,000/-
2.	Associate Professor & Assistant Professor who has completed 03 years service in the AGP 8000 under 6 th CPC.	Rs. 2250/-	Rs. 1125/-	Rs. 70,000/-
3.	Assistant Professors (not covered above).	Rs. 1750/-	Rs. 875/-	Rs. 52,000/-

Note: TA will be paid separately @ Rs. 800/- & Rs. 500/- for Professor/Associate Professor & Assistant Professor respectively.

This issues with the approval of the Competent Authority.

(Prof. Madhusudan Singh) Registrar

Dated: 47/09/2022

F.No.2-592/2013/Estt/DTU/1707

Copy to:-

- 1. P.A. to VC for kind information of the Hon'ble Vice Chancellor.
- 2. P.A. to Registrar for kind information of the Registrar.
- 3. All Deans/HoDs/Branch Incharges with request for vide circulation among their respective department/branches., DTU.
- 4. Controller of Finance, DTU.
- 5. Drawing & Disbursing Officer (DDO).
- 6. Head, Computer Centre with request to upload the same on University website.
- 7. Guard file.

(Dr. R. Kaushik)

Dy. Registrar (Estt.)

Annexure-II

Essential	D 1 (D 1	Remuneration	
Qualifications	Relevant Branch	per hr for	
		Guest Faculty	
	Advanced Communication and Information	-	
	System, Advanced Electronics &		
DE/DE 1 1	Communication Engineering,		
B.E./B.Tech and	Artificial Intelligence,		
M.E./ M.Tech. in	Computer and Communication		
relevant branch	Engineering, Computer Applications,		
with 1st class or	Computer Engineering, Computer		
equivalent either	Engineering & Applications, Computer		
in B.E./B.Tech or	Networking, Computer Science, Computer		
M.E./ M.Tech.	Science & Engineering, Computer Science &		
from a recognized	Information Technology, Computer		
University	Technology & Applications, Computer		
(OD)	Science & Technology, Computer Science		
'OR'	and Systems Engineering, Computer		
104 1 3 5 0 4 1	Technology, Electrical & Electronics	As per the	
1st class MCA and	Engineering, Electrical Engineering,	university	
1 st class in M.Tech.	Electronic & Computer Engineering,	norms	
in relevant branch	Electronic Engineering, Electronic &	(Remuneration	
from a recognized	Communication Engineering, Electronics &	and Travel	
University	Instrumentation, Electronics &	allowances)	
(OD)	Telecommunication Engineering,	,	
'OR'	Information & Communication Technology,		
1 at 1	Information Engineering, Information		
1 st class or	Science & Engineering, Information Science		
equivalent in	& Technology, Information Security,		
B.E./B.Tech in	Information Systems, Information		
relevant branch/1st	Technology, Information Technology &		
class in MCA and	Engineering, Mathematics & Computing,		
Ph.D. in relevant	Mobile & Pervasive Computing, Software		
branch from a	Engineering, Software Systems, Software		
recognized	Technology, Software Testing, VLSI Design,		
University	Web Designing, Web Technologies, 3-D		
	Animation & Graphics, Applied Electronics		
	and Instrumentation, Microelectronics		